

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Pegler, P.) Examiner:
)
Serial No.:) Art Unit:
)
Filed: February 10, 2004)
)
For: METHOD FOR A JUNCTION)
FIELD EFFECT TRANSISTOR)
WITH REDUCED GATE)
CAPACITANCE)
_____)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT FOR DIVISIONAL

Dear Sir:

As a preliminary amendment for the present Divisional,
Applicant respectfully requests the Examiner to consider the
following amendments.

IN THE TITLE

Please delete the present Title of the Invention and insert in place thereof the following Title, --METHOD FOR A JUNCTION FIELD EFFECT TRANSISTOR WITH REDUCED GATE CAPACITANCE--.